



GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi – 110078

EXAMINATION DIVISION

Conduct Branch-I

FINAL THEORY DATE SHEET FOR END TERM EXAMINATIONS (DECEMBER 2016)

M. Tech. (IT/CSE/IS/ECE/DC/SP/RA/RF & Microwave/VLSI Design/DWC) – Regular Programme

Date/Day	Morning Session (10:00 AM to 1:00 PM)	Evening Session (2:00 PM to 5:00 PM)
05.12.2016 Monday	MECS-601 Advance Data Structure MEEC-601 Opto Electronics & Optical Communication MEIS-601 Advanced Computer Network MEVS-609 Algorithm Analysis and Design MERA-601 Computational Mathematics	
06.12.2016 Tuesday		MECS-701 Data ware Housing & Data Mining/ MECS-701 Advanced Data Warehousing & Data Mining MEEC-701 Adhoc Sensor Network MESP-701 Advanced Digital Signal Processing MERF-701 Electromagnetic Interference Compatibility in System Design MEVS-701 Algorithm for VLSI Physical Design Automation MERA-701 Computer Integrated Manufacturing
07.12.2016 Wednesday	MECS-603 Advance Software Engineering MEDC-601 Advance Digital Communication MEVS-601 Digital System Design using Verilog MERA-603 Robotics Engineering	
08.12.2016 Thursday		MESP-703 Statistical Signal Processing MERF-703 Microwave & Millimeter Integrated Circuits MEIS-701 Mobile & wireless Network Security MERA-703 Computer Vision MEEC-713 MEMS and Sensor Technology

Date/Day	Morning Session (10:00 AM to 1:00 PM)	Evening Session (2:00 PM to 5:00 PM)
09.12.2016 Friday	MECS-605 Advances in Data & Computer Communication MEDC-603 Optical Fiber Communication Systems MESP-601 Signal Theory MEIT-601/MEIT-603 Introduction to Computer Security MERA-605 Introduction to Manufacturing Systems	
10.12.2016 Saturday		MECS-703 Advance Software Testing MEDC-701 Advance Mobile Computing MEVS-703 VLSI Design Test and Testability MERA-713 Soft Computing MEEC-715 Broadband Access Technology
12.12.2016 Monday	MECS-609 Enterprise Computing using JAVA MEVS-603 VLSI Technology MERA-607 Introduction to Electrical and Electronics Systems	
14.12.2016 Wednesday		MESP-707 Speech Signal Processing MERF-705 RFIC MEIS-705 Biometric Security MEEC-721 Microwave Planar Transmission & Circuits MERA-719 Simulation and Modeling
15.12.2016 Thursday	MECS-607/MECS-613 Advanced Operating Systems MEDC-605 Advanced Signal Processing MESP-603 Analog Signal Processing MERF-605 Radio Frequency & Microwave Passive Circuit Design MEIS-605 Cryptography Foundation & its Applications MERA-609 Control System	
16.12.2016 Friday		MEIT-703/MEIT-725 Information Theory & Coding MEDC-703 Broadband Communication System & Networks MEVS-713 MEMS and IC Integration MERA-721 Machine Learning
17.12.2016 Saturday	MECS-611 Computational Techniques MEEC-607 Advance Computer Network MEVS-605 Advance VLSI Design MERA-619 Mechatronics Systems and Applications MERF-601 Advanced Electromagnetic Engineering	

Date/Day	Morning Session (10:00 AM to 1:00 PM)	Evening Session (2:00 PM to 5:00 PM)
19.12.2016 Monday		MESP-715 Selected Topic in Analog IC Design MERF-707 Smart Antennas for Mobile Communication MECS-707 E-Commerce & Applications MEEC-707 Neural Networks MEIS-711 Digital Defence Issue in Security
20.12.2016 Tuesday	MERF-603 Microwave Theory & Circuits MEDC-607 Computer Communication Networks MESP-607 Broadband Communication and Information Systems	
21.12.2016 Wednesday		MECS-711 Software Quality Management MEEC-711 Cryptography and Coding MEDC-705 High Performance Communication Network MEVS-705 Analog-Mixed Signal Design MERF-711 RF MEMS MEEC 709 Multimedia Communications
22.12.2016 Thursday	MEVS-611 Wireless Network MECS-607 Advance Computer Architecture	
23.12.2016 Friday		MEDC-709 Selected Topics on recent Technologies in Wireless & Mobile Communication MERF-715 Radar Signal Processing MERF-717 Wireless Adhoc Networks MEIS-719 Security Issues in Information System
24.12.2016 Saturday	MERF-607 Radar Systems MEIS-609 Decision Support System and Methods	MESP-709 VLSI Design of DSP Circuits MECS-705 Cloud Computing
26.12.2016 Monday		MECS-717 Cyber Crime Investigation and Cyber Forensics MEDC-707 Spread Spectrum Technique MECS-715/MEIT-715 Advanced Multimedia MEVS-709 Designing with FPGA

**Examination Centre: University School of Information & Communication Technology, E-Block, Room No. EFC- 315,
GGs Indraprastha University, Sector 16-C, Dwarka, New Delhi – 110078.**

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For any clarification and query please contact:

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(A.D. Lamba)
Dy. Registrar (Examinations)

(Prof. Pravin Chandra)
Controller of Examinations(Operations)

Dated : 28.11.2016