



# GURU GOBIND SINGH INDRAPRASTHA UNIVERSITY

Sector 16-C, Dwarka, New Delhi – 110078

## EXAMINATION DIVISION

### **Conduct Branch-I**

FINAL PRACTICAL DATE SHEET FOR END TERM EXAMINATIONS (NOVEMBER 2016)

**Programme: M. Tech. (CSE, IT and VLSI Design)**

**Only for C-DAC Students {Regular & Reappear}**

Date/Day	Course/Semester	Course Code	Time: 9:30 AM onwards
<b>15.11.2016 Tuesday</b>	CSE/ 1st Sem.	MECS-651	Advanced Data Structures Lab
	IT /1st Sem.	MEIT-651	Advanced Data Structures Lab
	VLSI /1st Sem.	MEVS-651	Digital System Design using Verilog Lab
	CSE/ 3rd Sem.	MECS-751	Advanced Data Warehousing & Data Mining
	IT/ 3rd Sem.	MEIT-751	Advanced Data Warehousing & Data Mining
	VLSI /3rd Sem.	MEVS-751	Algorithm for VLSI Physical Design Automation Lab
<b>16.11.2016 Wednesday</b>	CSE/ 1st Sem.	MECS-653	Advance Software Engineering Lab
	IT /1st Sem.	MEIT-653	Advance Software Engineering Lab
	VLSI /1st Sem.	MEVS-653	Advanced VLSI Design Lab
	CSE/ 3rd Sem.	MECS-753	Software Testing
	IT/ 3rd Sem.	MEIT-759	Information Theory & Coding
	VLSI/ 3rd Sem.	MEVS-753	VLSI Design Test and Testability Lab
<b>17.11.2016 Thursday</b>	CSE/ 1st Sem.	MECS-655	Advances in Data & Computer Communications Lab
	IT/ 1st Sem.	MEIT-659	Advances in Data & Computer Communications Lab
	VLSI /1st Sem.	MEVS-655	Algorithm Analysis and Design Lab
<b>21.11.2016 Monday</b>	CSE/ 3rd Sem.	MECS-757	Minor Project
	IT/ 3rd Sem.	MEIT-757	Minor Project
	VLSI/ 3rd Sem.	MEVS-757	Minor Project

**Note:**

- 1. Practical Exams of students will be held at their respective Institutes/Departments.**
- 2. For Detailed Schedule / Groups students may contact to the Office of Dean/Director / Head of the respective Institute / Department.**

**For any clarification and query please contact:**

Section Officer (Conduct-I)	011-25302263
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**(A.D. Lamba)**  
**Dy. Registrar (Examinations)**

**(Prof. Pravin Chandra)**  
**Controller of Examinations(Operations)**

**Dated : 04.11.2016**