



**Track : VLSI Design and Automation (Tentative Schedule)**

Technical Session- 1 VLSI		VLSI Design and Automation-I	
Venue: [212 D ]		Date: 11-03-2016	
		Time: 3:30-5:00pm	
Sr No.	Manuscript ID	Title	Author
1.	1570242117	Implementation of Asynchronous Pipeline using Transmission Gate logic	Naman Saxena
2.	1570242002	FPGA Implementation of RSA based on Carry Save Montgomery Modular Multiplication	Rupali Verma
3.	1570241823	AC Analysis of Junctionless Double Surrounding Gate (JLDSG) MOSFET for Tera Hertz Applications	Sonam Rewari
4.	1570241785	Simulation and Analysis of Si GAA Nanowire Tunneling FET	Neel Chatterjee
5.	1570241946	Dual Workfunction Tunnel Field-Effect Transistor with shifted gate for ambipolar suppression and ON current improvement	Bhagwan Raad
6.	1570241514	80V InGaAs Lateral Trench Power MOSFET	Manoj Adhikari
7.	1570240195	MATLAB/SIMULINK Based Time-Domain Behavioral Modeling Of Multibit Sigma-Delta Converters	Mudasir Bashir

Technical Session -2 VLSI		VLSI Design and Automation-II	
Venue: E Block Seminar Hall		Date: 12-03-2016	
		Time: 3:30-5:00pm	
Sr No.	Manuscript ID	Title	Author
8.	1570235638	Pre-scalar for Diode Free Adiabatic Logic Family	Neeta Pandey
9.	1570227653	Performance Evaluation of Subthreshold Schmitt Trigger Using Body Bias Techniques	Meenali Janveja
10.	1570241746	Implementation of PFSCl Demultiplexer	Kirti Gupta
11.	1570241945	Comparative Analysis of Different Lottery Bus Arbitration Techniques for SoC Communication	Bhawna Tiwari
12.	*1570235814	Usage of Lowpass Filters for Miniaturization of Microstrip Branch-Line Hybrid Couplers	Denis Letavin
13.	*1570233045	Optimized PI Tuning of Power System Stabilizers Using Coefficient Diagram Method	Durga G
14.	#1570222554	Design and Analysis of Frequency Selective Surface at Ka/K/Ku Band for antenna miniaturization	Ankush Kapoor

Technical Session-3 VLSI		VLSI Design and Automation-III	
Venue: E Block Seminar Hall		Date: 13-03-2016	
		Time: 9:30-11:00am	
Sr No.	Manuscript ID	Title	Author
15.	1570221194	Realization of Current Mode KHN-Equivalent Biquad Employing Third Generation Current Conveyor	Tajinder Arora
16.	1570235162	An Improved Performance Parameters Evaluation of The Diode Rectifiers	Tapas Halder
17.	1570235159	An Adverse Effect of The Harmonics for The Power Quality Issues	Tapas Halder
18.	*1570237845	Study of effect of dielectric superstrate on resonance frequency of patch antenna and measurement of dielectric constant	Pranavsesh VS
19.	1570238565	Diagnosis of Narcolepsy Sleep Disorder for Different Stages of Sleep Using Short Time Frequency Analysis of PSD Approach Applied on EEG Signal	Mohd Siddiqui
20.	1570242116	PFSCl based Linear Feedback Shift Register	Abhishek Tyagi

NOTE :- # :- Subject to the approval by the Screening Committee.

- : Registration Fees proof not received/pending



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21.	*1570226367	Single Input Multiple Output Multifunction Filter Employing Current Differencing Buffered Amplifier	Udit Rana

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